Faculty CV

1. Name of Faculty: Mrs. Vinaya Gopalrao Rajeshwarkar

2. Present Position: Lecturer in Electronics Engineering

3. Address: Near Agrasen Bhavan, Zingnappa Galli, Latur- 413512

4. Mobile No.: 9423776142

5. Email – ID: <u>vinaya.rajeshwarkar@gmail.com</u>

6. Date of Birth: 15/05/1981



Qualifications:

Sr.	Examination	Institute Name	Board/University	% of	Class Award
No.				marks	
1.	Diploma in Electronics	Government	M.S.B.T.E.,	78	First class with
	&Tele Communication	Polytechnic,	Mumbai		Distinction
		Osmanabad			
2.	B. E. in Electronics	Cummins	Pune University,	64	First class
	&Tele Communication	College of	Pune		
		Engineering,			
		Pune			
3.	M.Tech in Electronics	S.G.G.S.I.E.&T.,	S.R.T.M.U.,	9.18	First class with
		Nanded	Nanded	CGPA	Distinction

Career Details:

Sr.	Organization	Designation	Period	Duration	Work done
No.					in Brief
1.	M.S.Bidve Engg	Assistant	22/07/2006	3.6 yrs	As per
	College, Latur	Professor	to		designation
			10/01/2011		
2.	V.D.F. School of Engg.,	Assistant	11/01/2011	4.6 yrs	As per
	Latur	Professor	to		designation
			22/07/2015		
3.	M.S.Bidve Engg	Assistant	23/07/2015	1.6 yrs	As per
	College, Latur	Professor	to		designation
			05/02/2017		
4.	Government	Lecturer	06/02/2017	2 yrs 5	As per
	Polytechnic, Sakoli,		to	months	designation
	Dist. Bhandara		15/07/2019		
5.	P.L.G.P., Latur	Lecturer	16/07/2019		As per
			to till date		designation

Specialized Training courses attended:

Sr. No.	Course/Training title	Training period	Training Place	Training Sponsored Agency
1	Induction training programme Phase -1	5/6/2017-16/6/2017	NIT Polytechnic, Nagpur	NITTTR Bhopal
2	Induction training programme Phase-2	10/12/2018-21/12/2018	NITTR Bhopal	NITTTR Bhopal

3	NBA Accreditation and Academic Reforms	14/01/2019-18/1/2019	G.P.Sakoli	MSBTE
4	Student Assessment and Evaluation	03/02/2020 - 07/02/2020	P.L.G.P. Latur	NITTTR Bhopal
5	Tools for online Teaching Learning Process	20/04/2020-17/05/2020	ICT Mode	MSBTE
6.	Research Trends in VLSI Design	20/05/2020 – 29/05/2020	ICT Mode	NITTTR,Chandigarh
7.	Pedagogical Techniques in Electronics and Electrical Engg.	10/08/2020 – 14/08/2020	ICT Mode	TEQIP- III
8.	Embedded System Design with ARM	Jan – March 2021	ICT Mode	NITTTR,Chandigarh
9.	Industrial Training on "Additive Manufacturing and Its Applications"	02/02/2022 – 15/02/2022	ICT Mode	NIELEIT, Aurangabad
10.	Cyber Security and Forensics	21/02/2022 – 25/02/2022	ICT Mode	PLGP, Latur
11.	Industrial Training on "Robotics Process Automation"	23/08/2022 – 03/09/2022	ICT Mode	NIELEIT, Aurangabad
12.	Module 1 to Module 8	July 2021 – September 2021	ICT Mode	AICTE (SWAYAM)

Signature

Mrs. Vinaya Gopalrao Rajeshwarkar